

IMPROVED BRUSHLESS DC MOTOR CONTROL ALGORITHM FOR REDUCING SOURCE CURRENT HARMONICS. SIMULATION STUDY

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ABSTRACT

Recently, BLDC motors have become very popular in wide application areas. The BLDC motor does not have a mechanical commutation, and is, consequently, more reliable than the classic DC motor. In recent years, with the increasing number of nonlinear loads drawing nonsinusoidal current, power quality has become a serious problem; hence an optimal control scheme must ensure the desired motor behavior and an undistorted current waveform in the network. This paper presents a new simplified control scheme for the BLDC motor, eliminating the disadvantages of the classic control scheme, and in addition keeping the source currents waveforms near sinusoidal, according to the standards.

INTRODUCTION

Considering the wide range of different types of motors, the choice of a specific motor type for a particular application generally is determined by performance and cost. Of all these, the brushless DC (BLDC) motor is gaining widespread use in various consumer and industrial applications. The BLDC motor features high efficiency and good controllability due to their linear speed/torque characteristics, giving predictable speed regulation.

A BLDC motor is a rotating electric machine where the stator is a classic 3-phase wound stator, like that of an induction motor, and the rotor has surface-mounted permanent magnets. When the wound stator is energized by a 3-phase alternating current, it creates a rotating magnetic flux that causes the rotor to rotate synchronously with it.

In a BLDC motor, the position of the rotor (and hence its permanent magnetic field) is sensed with respect to the stator coils (phases), and the supply current is switched electronically (commutated) to the appropriate phases. BLDC motor control systems often incorporate either internal or external position sensors to sense the actual rotor position. Alternatively, the rotor position can be detected without sensors by measuring the back-EMF in each stator winding (Cyan Technology 2007).

The BLDC motor is driven by trapezoidal currents coupled with the given rotor position. They offer longer life and less maintenance than conventional brushed DC motors. Some other advantages over brushed DC motors and induction motors are: better speed versus torque characteristics, noiseless operation and higher speed ranges. In addition, the ratio of torque delivered to the size of the motor is higher, making them useful in applications where space and weight are critical factors. In recent years, with the increasing number of nonlinear loads drawing nonsinusoidal currents, power quality distortion has become a serious problem in electrical power systems due to the increasing harmonics disturbance. These harmonics currents causes adverse effects in power systems such as overheating, perturbation of sensitive control and communication equipment, capacitor blowing, motor vibration, excessive neutral currents, resonances with the grid and low power factor (Maswood et al. 2002). Hence an optimal control scheme must ensure the desired motor behavior and an undistorted current waveform in the network.

This paper presents a new simplified control scheme for the BLDC motor, which eliminates the disadvantages of the classical control scheme, and in addition, also keeps the network current waveform sinusoidal. The new scheme is simulated using Matlab SimPowerSystems.

CLASSICAL BLDC CONTROL SCHEME

The classical BLDC control scheme is shown in figure 1. Typically it contains two power electronics bridges – first one is an uncontrolled rectifier with diodes and the second one is a three phase controlled PWM inverter. Usually, between the two bridges is a capacitor (King et al. 2008).

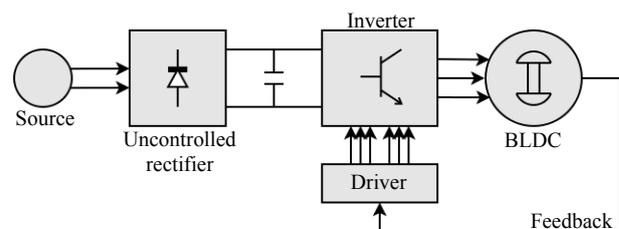


Figure 1: Classical BLDC Control Scheme

The torque and the speed of the BLDC motor depend on the magnetic field of the windings, consequently on the

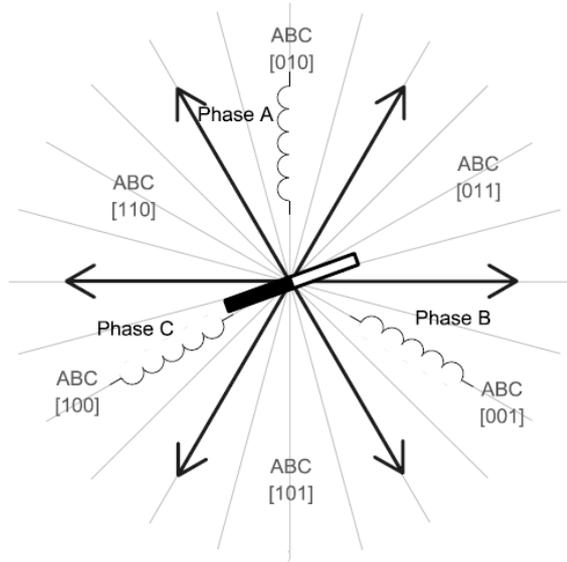


Figure 5: Stator Flux Vectors and Hall Sensors Output

While commutation ensures proper rotor rotation of the BLDC motor, the motor speed depends on the amplitude of the applied voltage. The amplitude of the applied voltage is adjusted by the controlled rectifier. The required speed and voltage are controlled by two PI controllers. The difference between the actual and required speed (and voltage) is the input of the PI controller. Using the difference, the PI controller control the duty cycle of PWM pulses fed to the controlled rectifier, corresponding to the voltage amplitude required to keep the desired speed. In addition, it needs to keep the source current harmonics free. The proposed control algorithm is presented in figure 6.

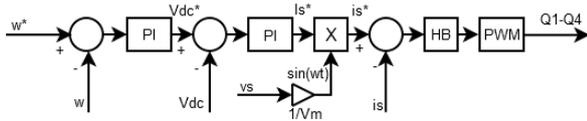


Figure 6: Proposed Algorithm for BLDC Control

SIMULATION RESULTS

The overall model of the proposed BLDC control is presented in figure 4 and the results were obtained using Matlab Simulink SimPowerSystems. The parameters of the chosen BLDC is seen in table 2.

Table 2: Motor parameters

Nominal voltage	220 V/50 Hz
Number of phases	3
Rotor type	Salient-pole
Back EMF waveform	Sinusoidal
Stator resistance	18.5 Ω
Inductances Ld and Lq	0.02682 H
Number of pole pairs	4

Figure 7 shows the reference and the obtained speed using the proposed BLDC control, under two reference speeds, 2000r/min and 3000r/min. It can be seen that the actual speed follows its reference speed very good, with a transient of 0.1s for the first reference, and with a transient of 0.05s for the second reference, thus proving the proposed BLDC control algorithm is very effective.

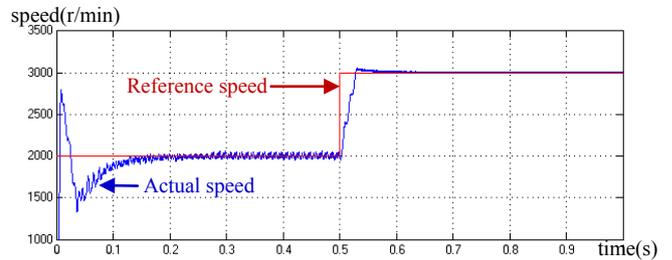


Figure 7: Reference and Actual Speed

Figure 8 shows the DC capacitor reference and actual voltage. Once the transient is over, the DC capacitor voltage is recovered to its reference value. It can be seen that its value follows up its reference, thus the objective of the proposed BLDC controller is achieved.

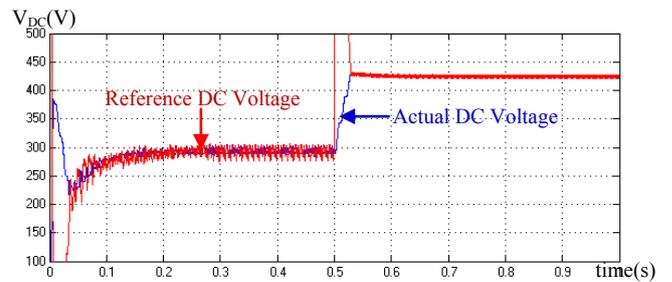


Figure 8: Reference and Actual DC Voltage

Figure 9 shows the simulation results of the source current obtained using the proposed BLDC control algorithm.

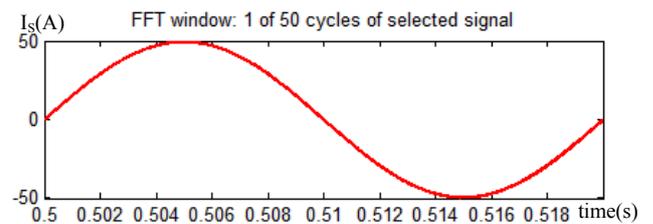


Figure 9: Source Current Waveform

Figure 10 shows the harmonic analysis of the source current. By using the proposed BLDC control algorithm, the source current has a total harmonic distortion of only 1.06%, thus meeting the limit of the harmonic standard of (IEEE STD. 519-1992).

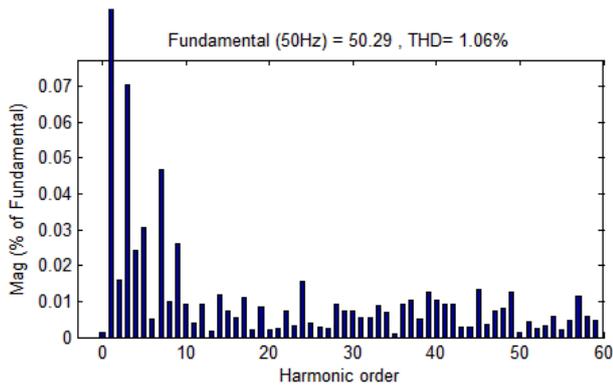


Figure 10: Harmonic Analysis of the Source Current

CONCLUSION

From the simulation results, it can be seen that the proposed BLDC control algorithm is a good alternative over the conventional BLDC control scheme, because not only that the motor operates as desired, but also the source current waveform is kept sinusoidal and with a harmonic distortion less than 5% according to the standards.

Still, this algorithm can be improved, by using it in threephased systems, so that the controlled rectifier can also provide the reactive power to the motor. In addition, it can act as a filter to the network, because it can balance unbalanced threephase systems.

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